CURRICULUM VITAE

NGUYEN DUY BANG

Embedded Software Engineer

https://bangnguyendev.github.io

⚧ Male

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📞 0784-140494

EDUCATION HISTORY

09/2015- 09/2019	Ho Chi Minh City University of Technology and Education, Vietnam. Major: Telecommunication - Electronics

OBJECTIVE

- In the next five years, I want to study more about information and job experiences so that I may become an exceptional engineer-project manager.
- Worked in a fast-paced atmosphere with several advancement chances and the ability to make a long-term commitment to a company where I can learn new skills and contribute to the company's success.

HONORS & AWARDS

01/2020- 12/2020	Team excellent of the year 2020 - Onsite BOSCH
09/2016- 09/2017	Encouraging Study Scholarship in 1st and 2nd semester 2016-2017.

SUMMARY INFORMATION

https://www.linkedin.com/in/bangnguyenduy/

- 2 years of experience in C/C++ programming.
- 1 years of experience in programming with Python.
- Good English communications skills, both verbal and writing.
- Working knowledge of processor architectures (ARM/ARM64, x86).
- Good at embedded system design methodology.
- Programming in C on Automotive Domain.
- Good Knowledge on micro-controller architecture and low-level device driver development.
- Expertise in Operating System Programming Languages Tools & Technology:
  - Microsoft Office: Word, Excel, PowerPoint.
  - C/C++, C#, Visual Basic (VBA), Python, Bash Shell.
  - SVN, Git (*https://bangnguyendev.github.io/*).
  - Win, Linux.

WORKING HISTORY

06/2019 – present:	Ban Vien Co., Ltd (Onsite at RVC, BOSCH)
12/2018 – 6/2019:	FPT Telecom

PROJECT REFERENCE AND SKILL SET

PROJECT NAME:	Full Phases – RZG CIP *(Civil Infrastructure Platform)*	
	Position:	Team leader
	Responsibilities:	Clarify requirements with Project Manager - Running project follows Scrum. - Test API module. - Use project tracking tool – Redmine, Jira. - Experience in QA/QC workflow.
	Duration:	01/2022 – present

PROJECT NAME:	Full Phases – MCAL drivers – AUTOSAR (RVC)	
	Position:	Team leader/sub-leader
	Responsibilities:	Clarify requirements with Project Manager - Running project follows V-model. - Test API with Davinci, Cantata, - Microsoft tools, AMDC, Enterprise Architect. - Use project tracking tool – Redmine, Jira. - Experience in QA/QC workflow.
	Duration:	10/2020 – 01/2022

PROJECT NAME:	ASW/PSW – MCAL drivers – AUTOSAR (BOSCH)	
	Position:	Team member/sub-leader
	Responsibilities:	Clarify requirements with Project Manager - Write test case, checklist and data to test. - Test API with Cantata, ASCET (support by Bash, Python…) - Microsoft tools, Daily report - Updated file to SharePoint, TCM sever. - Experience in QA/QC workflow
	Duration:	02/2020 – 9/2020

PROJECT NAME:	D1x – MCAL drivers – AUTOSAR (RVC)	
	Position:	Team member
	Responsibilities:	Clarify requirements with Project Manager - Write test case, checklist and data to test - Test API with postman tool - Test API with Davinci, Cantata, - Microsoft tools, AMDC, Enterprise Architect. - Use project tracking tool – Redmine, Jira - Experience in QA/QC workflow
	Duration:	09/2019 – 2/2020

PROJECT NAME:	U2A – MCAL drivers – AUTOSAR (RVC)	
	Position:	Team member
	Responsibilities:	Clarify requirements with Project Manager - Write test case, checklist and data to test - Test API with postman tool - Test API with Davinci, Cantata, - Microsoft tools, AMDC, Enterprise Architect. - Use project tracking tool – Redmine, Jira - Experience in QA/QC workflow
	Duration:	06/2019 – 09/2019